

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (π -MOSV)

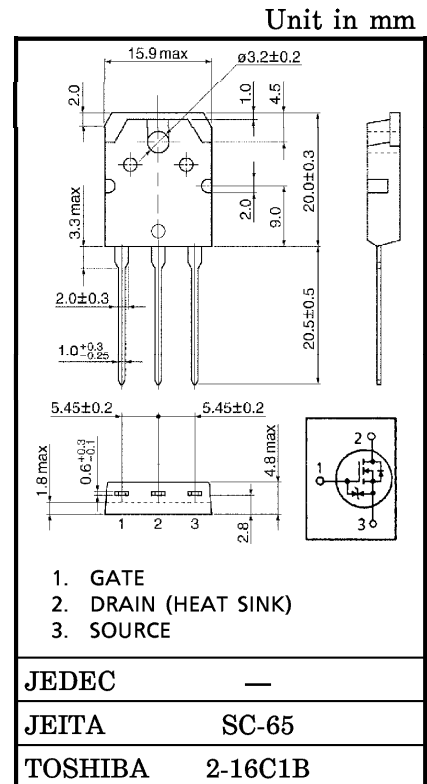
2SK3176

SWITCHING REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

- Low Drain-Source ON Resistance : $R_{DS(ON)} = 38 \text{ m}\Omega$ (Typ.)
- High Forward Transfer Admittance : $|Y_{fs}| = 30 \text{ S}$ (Typ.)
- Low Leakage Current : $I_{DSS} = 100 \mu\text{A}$ (Max.) ($V_{DS} = 200 \text{ V}$)
- Enhancement-Model : $V_{th} = 1.5 \sim 3.5 \text{ V}$ ($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	V_{DSS}	200	V
Drain-Gate Voltage ($R_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	200	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current	DC (Note 1)	I_D	30 A
	Pulse (Note 1)	I_{DP}	120 A
Drain Power Dissipation ($T_c = 25^\circ\text{C}$)	P_D	150	W
Single Pulse Avalanche Energy (Note 2)	E_{AS}	925	mJ
Avalanche Current	I_{AR}	30	A
Repetitive Avalanche Energy (Note 3)	E_{AR}	15	mJ
Channel Temperature	T_{ch}	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	$-55 \sim 150$	$^\circ\text{C}$



Weight : 4.6 g (Typ.)

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th(ch-c)}$	0.833	$^\circ\text{C/W}$
Thermal Resistance, Channel to Ambient	$R_{th(ch-a)}$	50.0	$^\circ\text{C/W}$

- (Note 1) : Please use devices on condition that the channel temperature is below 150°C .
 (Note 2) : $V_{DD} = 50 \text{ V}$, $T_{ch} = 25^\circ\text{C}$ (initial), $L = 1.66 \text{ mH}$, $R_G = 25 \Omega$, $I_{AR} = 30 \text{ A}$
 (Note 3) : Repetitive rating ; Pulse Width Limited by maximum junction temperature.

**This transistor is an electrostatic sensitive device.
Please handle with caution.**

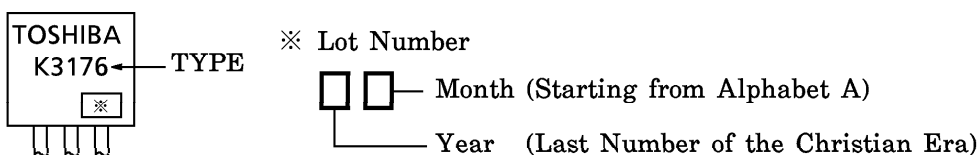
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

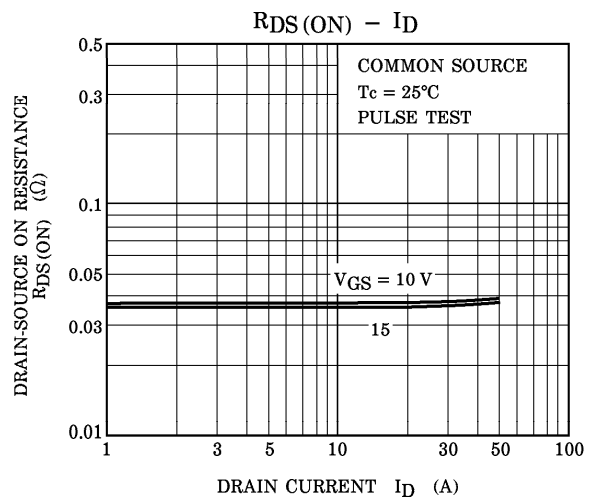
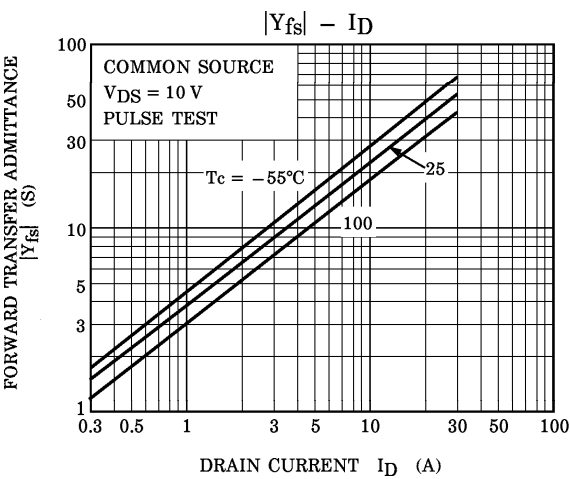
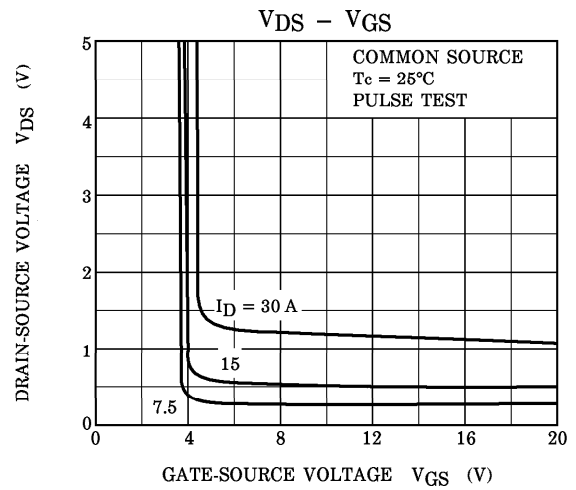
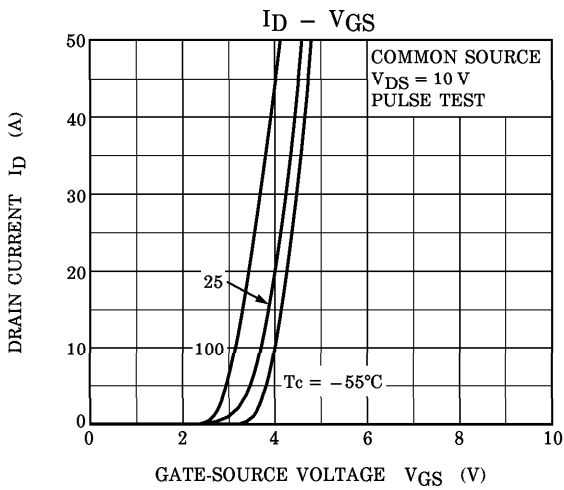
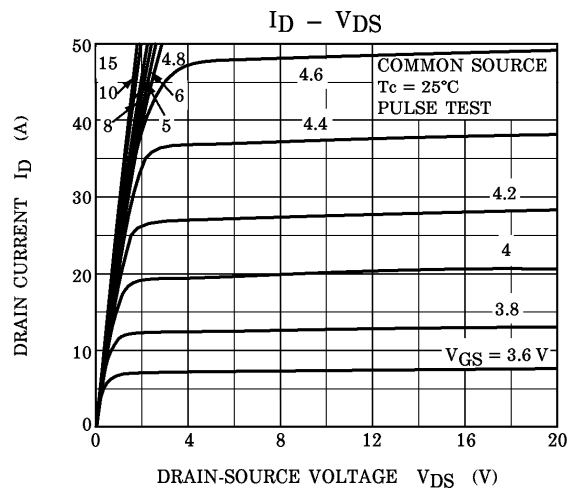
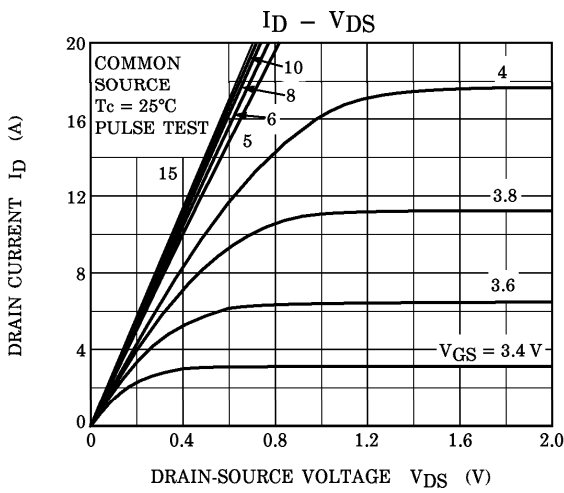
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		IGSS	VGS = ±16 V, VDS = 0 V	—	—	±10	μA
Drain Cut-off Current		IDSS	VDS = 200 V, VGS = 0 V	—	—	100	μA
Drain-Source Breakdown Voltage		V(BR)DSS	ID = 10 mA, VGS = 0 V	200	—	—	V
Gate Threshold Voltage		Vth	VDS = 10 V, ID = 1 mA	1.5	—	3.5	V
Drain-Source ON Resistance		RDS(ON)	VGS = 10 V, ID = 15 A	—	38	52	mΩ
Forward Transfer Admittance		Yfs	VDS = 10 V, ID = 15 A	15	30	—	S
Input Capacitance		Ciss	VDS = 10 V, VGS = 0 V f = 1 MHz	—	5400	—	pF
Reverse Transfer Capacitance		Crss		—	580	—	
Output Capacitance		Coss		—	1900	—	
Switching Time	Rise Time	tr		—	15	—	ns
	Turn-on Time	ton		—	55	—	
	Fall Time	tf		—	25	—	
	Turn-off Time	toff		—	190	—	
Total Gate Charge (Gate-Source Plus Gate-Drain)		Qg	VDD ≐ 160 V, VGS = 10 V ID = 30 A	—	125	—	nC
Gate-Source Charge		Qgs		—	80	—	
Gate-Drain ("Miller") Charge		Qgd		—	45	—	

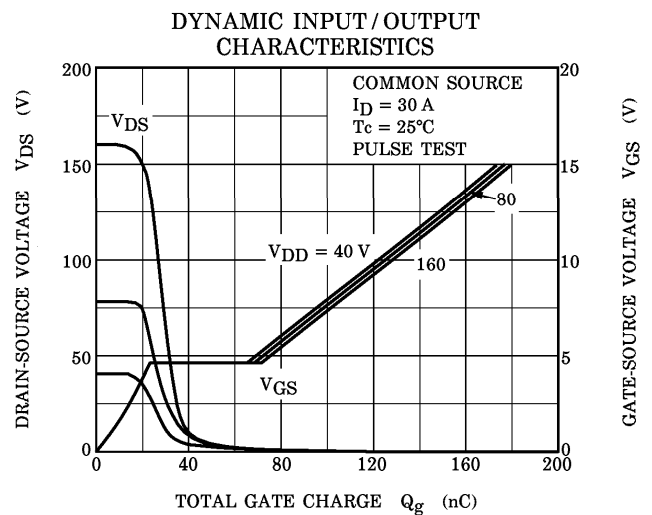
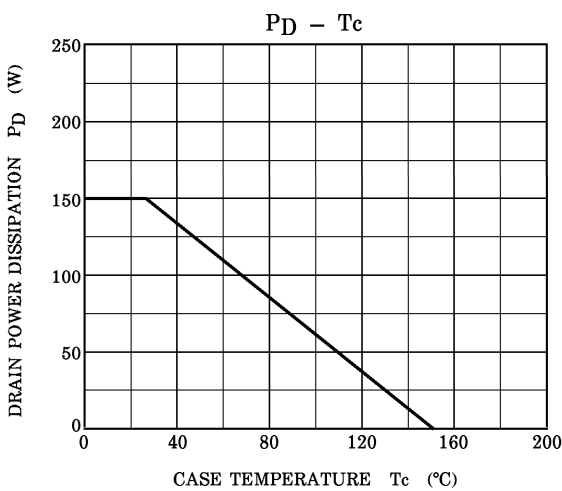
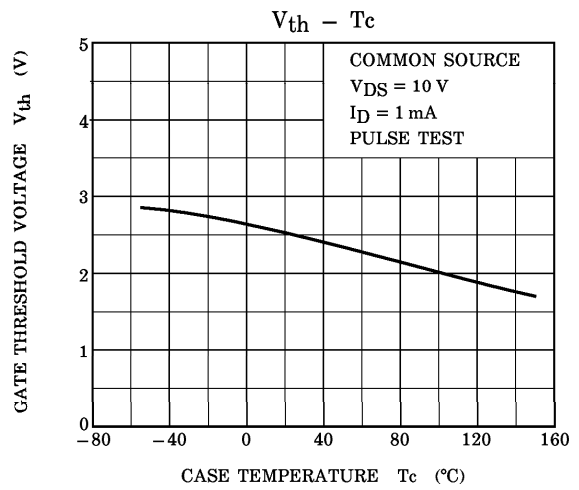
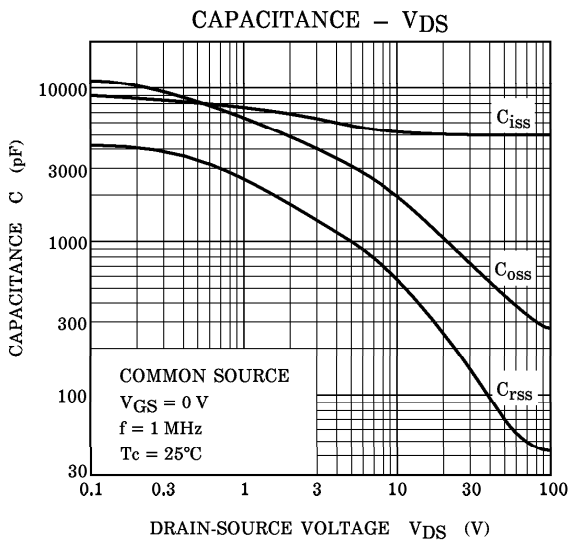
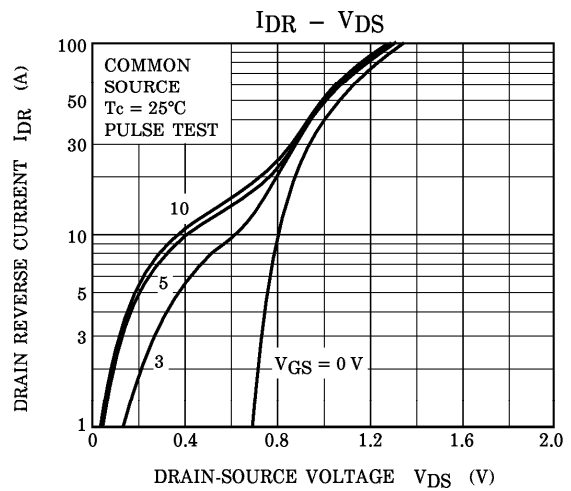
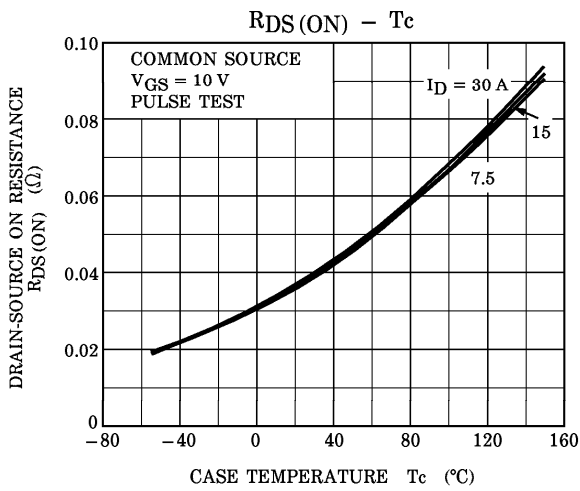
SOURCE-DRAIN RATINGS AND CHARACTERISTICS (Ta = 25°C)

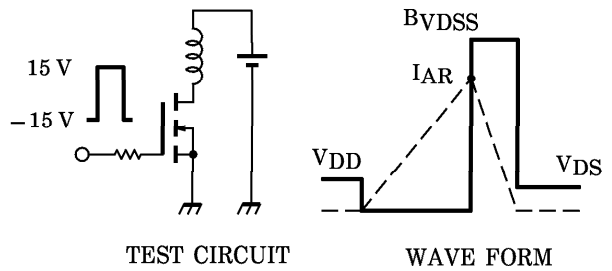
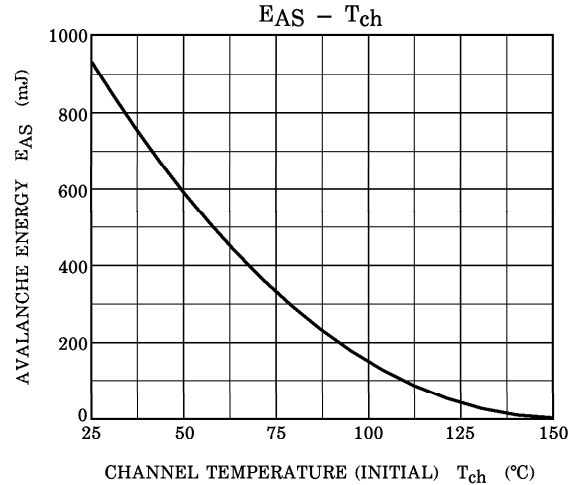
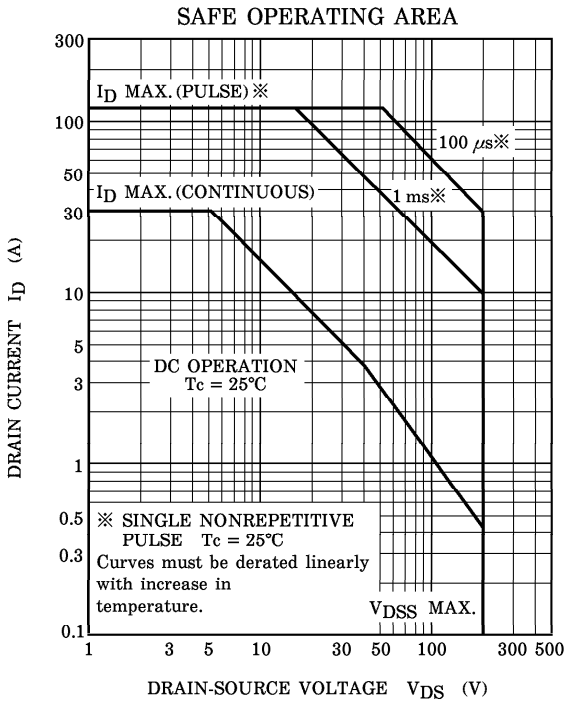
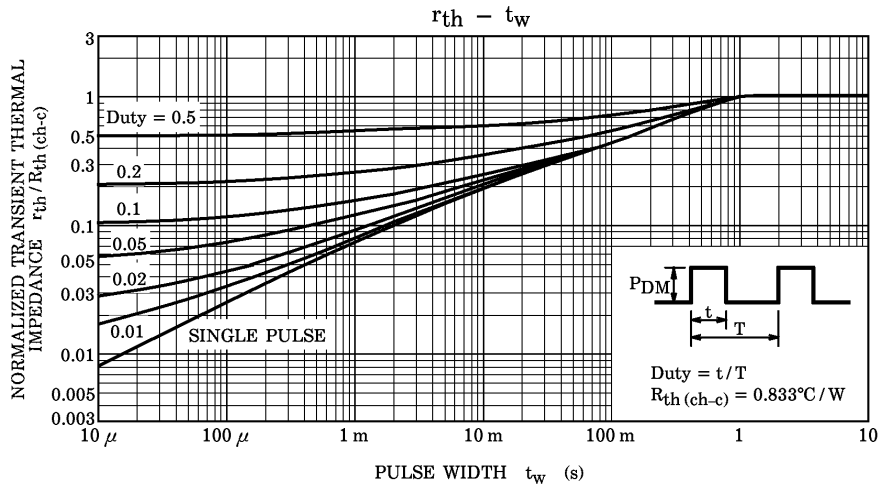
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current (Note 1)	IDR	—	—	—	30	A
Pulse Drain Reverse Current (Note 1)	IDRP	—	—	—	90	A
Forward Voltage (Diode)	VDSF	IDR = 30 A, VGS = 0 V	—	—	-2.0	V
Reverse Recovery Time	trr	IDR = 30 A, VGS = 0 V	—	270	—	ns
Reverse Recovery Charge	Qrr	dIDR / dt = 100 A / μs	—	3.0	—	μC

MARKING









$$R_G = 25 \Omega$$

$$V_{DD} = 50 \text{ V}, L = 1.66 \text{ mH} \quad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$

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